APPLICATION FOR UNITED STATES PATENT DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I declare that my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor if only one name is listed below, or an original, first and joint inventor if plural inventors are named below, of the subject matter which is claimed and for which a patent is sought on the invention entitled as set forth below, which is described in the attached specification; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, section 1.56;

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

TITLE OF INVENTION:				
METHOD AND APPARATUS TO REDUCE STORAGE NODE DISTURBANCE IN FERROELECTRIC MEMORY				
	PPOINT THE FOLLOWING ATTORNEYS TO PROSI D TRADEMARK OFFICE CONNECTED THEREWIT			
Practitioners at Customer Number: 23494 Thomas G. Eschweiler, Reg. No. 36,981 Eric M. Highman, Reg. No. 43,672 Gregory J. Adams, Reg. No. 44,494 William J. Cooper, Reg. No. 44,629				
SEND CORRESPONDENCE TO:		DIRECT TELEPHONE CALLS TO:		
Jacqueline J. Garner Texas Instruments Incorporated PO Box 655474, M/S 3999 Dallas, TX 75265		Jacqueline J. Garner (214) 532-9348		
NAME OF INVENTOR: (1)	NAME OF INVENTOR: (2)	NAME OF INVENTOR: (3)		
Sudhir Kumar Madan	Sung-Wei Lin	Hugh P. McAdams		
RESIDENCE (City and State Only)	RESIDENCE (City and State Only)	RESIDENCE (City and State Only)		
Richardson, Texas	Plano, Texas	McKinney, Texas		
Post Office Address: 610 Saint George Richardson, Texas 75081	Post Office Address: 8020 Lynores Way Plano, Texas 75025	Post Office Address: 5416 County Road 206 McKinney, Texas 75071		
Country of Citizenship: United States	Country of Citizenship: United States	Country of Citizenship: United States		
Signature of Inventor. Indhir Mada	Signature of Inventor:	Signature Of Inventor: Ary W.		
Date: 03-18-2004	Date:	Arch M. William Date: 3/18/04		

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METHOD AND APPARATUS TO REDUCE STORAGE NODE DISTURBANCE IN FERROELECTRIC MEMORY				
POWER OF ATTORNEY: I HEREBY A TRANSACT ALL BUSINESS IN THE PATENT AN	PPOINT THE FOLLOWING ATTORNEYS TO PROSID TRADEMARK OFFICE CONNECTED THEREWIT	ECUTE THIS APPLICATION AND H.		
Practitioners at Customer Number: 2	3494 Thomas G. Eschweiler, Reg. No. 43,6 Eric M. Highman, Reg. No. 43,6 Gregory J. Adams, Reg. No. 44 William J. Cooper, Reg. No. 44	672 1,494		
SEND CORRESPONDENCE TO: Jacqueline J. Garner Texas Instruments Incorporated PO Box 655474, WS 3999 Dallas, TX 75265		DIRECT TELEPHONE CALLS TO: Jacqueline J. Garner (214) 532-9348		
NAME OF INVENTOR: (1)	NAME OF INVENTOR: (2)	NAME OF INVENTOR: (3)		
Sudhir Kumar Madan	Sung-Wei Lin	Hugh P. McAdams		
RESIDENCE (City and State Only)	RESIDENCE (City and State Only)	RESIDENCE (City and State Only)		
Richardson, Texas	Plano, Texas	McKinney, Texas		
Post Office Address: 610 Saint George Richardson, Texas 75081	Post Office Address: 8020 Lynores Way Plano, Texas 75025	Post Office Address: 5416 County Road 206 McKinney, Texas 75071		
Country of Citizenship: United States	Country of Citizenship: United States	Country of Citizenship: United States		
Signature of Inventor:	Signature of Inventor: Sungue: Li	Signature Of Inventor:		
Date:	Date: 3-18-2004	Date:		

ATTORNEY DOCKET NO. TI-36818

APPLICATION FOR UNITED STATES PATENT DECLARATION AND POWER OF ATTORNEY

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TITLE OF INVENTION:				
METHOD AND APPARATUS TO REDUCE STORAGE NODE DISTURBANCE IN FERROELECTRIC MEMORY				
POWER OF ATTORNEY: I HEREBY APPOINT THE FOLLOWING ATTORNEYS TO PROSECUTE THIS APPLICATION AND TRANSACT ALL BUSINESS IN THE PATENT AND TRADEMARK OFFICE CONNECTED THEREWITH.				
Practitioners at Customer Number: 23494 Thomas G. Eschweiler, Reg. No. 36,981 Eric M. Highman, Reg. No. 43,672 Gregory J. Adams, Reg. No. 44,494 William J. Cooper, Reg. No. 44,629				
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NAME OF INVENTOR: (1)	NAME OF INVENTOR: (2)	NAME OF INVENTOR: (3)		
Anand Seshadri	Jarrod Eliason			
RESIDENCE (City and State Only)	RESIDENCE (City and State Only)	RESIDENCE (City and State Only)		
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Post Office Address: 1404 Calhoun Lane Plano, Texas 75025	Post Office Address: 4851 Saddie Ridge Drive Colorado Springs, Colorado 80922	Post Office Address:		
Country of Citizenship: United States	Country of Citizenship: United States	Country of Citizenship:		
Signature of Inventor: August Ageloda Date:	Signature of Inventor:	Signature Of Inventor:		
Date: 3/18/04	Date: 3-18-04	Date:		